



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: **Arthur Allan Bayot**
Serial No.: **09/963,493**
Filed: **09/27/2001**
For: **Methods Of And Apparatus For Manufacturing
Ball Grid Array Semiconductor Device Packages**

Docket No.: **TI-33474**
Examiner: **Coleman, Wm.**
Art Unit: **2823**
Conf. No.: **8827**

ELECTION

January 9, 2003

Assistant Commissioner For Patents
Washington, D.C. 20231

Dear Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)

I hereby certify that on this day the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner For Patents, Washington, D.C. 20231.

Elizabeth Austin

Date

This election is offered in response to the Examiner's restriction requirement dated December 17, 2002. The Examiner has required restriction to one of the groups of: Group I, Claims 1-5 or Group II, Claims 6-11.

Applicant hereby elects to pursue Group I, Claims 1-5 without traversing the Examiner's restriction requirement.

Respectfully submitted,

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